**Diego Moreno**

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**EDUCATION**

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| **University of Texas at Austin** | Bachelor of Science in Electrical Engineering  *Overall GPA: 3.2* | Fall 2014 |

**EXPERIENCE**

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| **Cirrus Logic** –*Entry Level Validation Engineer*; Austin, TX.   * Conceptualized and developed framework software for multi-DUT validation tests * Assisted in the development and execution of DSP validation tests * Knowledgeable in multi-mode mixed-signal simulation tools, mixed-signal macro-modeling, and test-bench creation, organization, and automation | Jan 2014 - Present |
| **Cisco Systems** – *GDS Lab Services Intern*; San Jose, California  • Supported Cisco’s RSPTG Engineering labs by configuring and deploying virtual machines, PDU’s, switches, servers, and routers  • Aided with the ordering, shipping, and receiving of lab equipment | Summer 2013 |
| **College Houses** – *IT & Computer Facilities Manager*; Austin, Texas   * Configured and maintained computer lab and equipment at 21st Street Co-op * Managed Ethernet network and DML for a 100+ student dormitory | Summer 2012 |

**PROJECTS**

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| **Senior Design Project – SeizeAlert; University of Texas at Austin**  Conceptualized, designed and developed a seizure detection and notification system for the Pebble smartwatch in the Android environment | 2014 |
| **Freelance Scripting for SXSW**  Enhanced film documentation and logistics using JavaScript and Google Apps Script | 2014 |
| **Digital Design**  Designed and programmed a piano in a computer keyboard with the use of a Xilinx Spartan board using VHDL programming | 2011 |
| **Echelon NodeBuilder Serial Communication**  Controlled a network of Echelon NodeBuilders through serial communication using a Neuron C based program | 2011 |

**PROFESSIONAL DEVELOPMENT**

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| **Alpha Lambda Delta & Phi Eta Sigma**  Honor societies for students who obtained and maintained 3.5 or higher GPA and are in the top 20% of their class | Present |

**ADDITIONAL INFORMATION**

**Excellent Writing and Communication Skills**

**Test/measurement:** Signal generators, oscilloscopes, digital power analyzers, soldering

**Assembly languages:** TI TMS320C6700 DSP, LC-3B ISA

**High-Level languages:** C, Java, JavaScript, HTML, CSS, Google Apps Script

**Software development:** TI Code Composer Studio

**Algorithm development:** LabVIEW, MATLAB

**Systems simulated:** Software-defined radio

**Real-time implementation:** Voiceband transceiver

**Team collaboration:** GitHub, Tortoise SVN, Assembla, Confluence, Jira

**Spoken languages:** English, Spanish, French

**Interests:** Healthy Cooking, Soccer, Motorcycles, Reading