**Diego Moreno**

diegomoreno@utexas.edu • linkedin.com/in/diegomoreno2

707 W. 21st St, Apt. 3C3 • Austin, TX 78705 • (512) 944-5248

**EDUCATION**

|  |  |  |
| --- | --- | --- |
| **Bachelor of Science in Electrical Engineering** | The University of Texas at Austin  Overall GPA: 3.32/4.00 | Dec 2014 |

**EXPERIENCE**

|  |  |
| --- | --- |
| **Entry Level Validation Engineer Intern** - Cirrus Logic   * Conceptualized and developed framework software for multi-DUT validation tests * Assisted in the development and execution of DSP validation tests * Gained experience in test-bench creation, organization, and automation | Aug 2013 – Aug 2014  Jan 2013 – May 2013 |
| **Freelance Scripting** - SXSW, LLC   * Enhanced film documentation and logistics using JavaScript and Google Apps Script | Spring 2014 |
| **GDS Lab Services Intern** - CiscoSystems  • Supported Cisco’s RSPTG Engineering labs by configuring and deploying virtual machines, PDU’s, switches, servers, and routers  • Aided with ordering, shipping, and receiving of lab equipment | May 2013 – Aug 2013 |
| **IT & Computer Facilities Manager** - CollegeHouses   * Managed Ethernet network and DML for 100+ student dormitory * Configured and maintained computer lab and equipment | May 2012 – Aug 2012 |

**PROJECTS**

|  |  |
| --- | --- |
| **Senior Design Project – SeizeAlert; The University of Texas at Austin**  • Conceptualized, designed and developed a seizure detection and notification system for Pebble smartwatch in Android environment | Fall 2013 – Spring 2014 |
| **Digital Design; Monterrey Institute of Technology**  • Designed and programmed piano in computer keyboard with use of Xilinx Spartan board using VHDL programming | Spring 2011 |
| **Echelon NodeBuilder Serial Communication; Monterrey Institute of Technology**  • Controlled network of Echelon NodeBuilders through serial communication using Neuron C based program | Spring 2011 |

**SKILLS**

|  |  |
| --- | --- |
| **Excellent Writing and Communication Skills**  **Test/measurement:** Signal generators, oscilloscopes, digital power analyzers, soldering  **Assembly languages:** TI TMS320C6700 DSP, LC-3B ISA  **High-Level languages:** C, Java, JavaScript, HTML, CSS, Google Apps Script  **Software development:** TI Code Composer Studio  **Algorithm development:** LabVIEW, MATLAB  **Systems simulated:** Software-defined radio  **Real-time implementation:** Voiceband transceiver  **Team collaboration:** GitHub, Tortoise SVN, Assembla, Confluence, Jira  **Spoken languages:** Fluent in English and Spanish, Basic French |  |

**ACCOMPLISHMENTS**

|  |  |
| --- | --- |
| **Recipient,** TAMS award, 2011-2014  **Active Member,** Alpha Lambda Delta, Honors Society  **Active Member,** Phi Eta Sigma, Honors Society  **Interests:** Cooking, Soccer, Motorcycles, Reading, Martial Arts, Musical Instruments |  |